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| SERIAL NUMBER<br>10/705,369 | FILING DATE<br>11/10/2003<br><br>RULE | CLASS<br>324 | GROUP ART UNIT<br>2829 | ATTORNEY<br>DOCKET NO.<br>550,698 |
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## \*\* CONTINUING DATA \*\*\*\*\*

N/A *J.M.H.*

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

N/A *J.M.H.*

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 02/12/2004

|   |  |                         |                       |                            |
|---|--|-------------------------|-----------------------|----------------------------|
| Foreign Priority claimed<br><input type="checkbox"/> yes <input checked="" type="checkbox"/> no   | STATE OR<br>COUNTRY<br>AZ  | SHEETS<br>DRAWING<br>16 | TOTAL<br>CLAIMS<br>12 | INDEPENDENT<br>CLAIMS<br>1 |
| 35 USC 119 (a-d) conditions<br>met <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after<br>Allowance | Verified and<br>Acknowledged<br><i>J.M.H.</i><br>Examiner's Signature Initials |                         |                       |                            |

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## TITLE

System for testing a group of IC-chips having a chip holding subassembly that is built-in and loaded/unloaded automatically

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|------------|---|---|
| FILING FEE | FEES: Authority has been given in Paper | <input type="checkbox"/> All Fees<br><input type="checkbox"/> 1.16 Fees ( Filing )<br><input type="checkbox"/> 1.17 Fees ( Processing Ext. of |
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